Abstract

The present invention is directed towards a method and system for implementing breakpoints in a processor system for debugging purposes. A separate memory space containing a breakpoint service routine is used and made available to the processor. When a breakpoint request is received, the main memory is switched out in favor of the separate memory space with the breakpoint service routine. The breakpoint service routine is then ran from the separate memory space. Upon the completion of the breakpoint service routine, control of the processor is switched back to the code in the main memory space.